

Biography Prof. Anirban Sengupta, Professor, IIT Indore

Prof. Anirban Sengupta (FIET, FBCS, FIETE, SMIEEE, Ph.D.) is a full Professor in the Department of Computer Science and Engineering at Indian Institute of Technology (IIT) Indore. He has over **330 publications** including **16 patents and 6 books (5 authored and 1 edited)**, to his credit.

1. He has held **3 distinguished awards and honors** from **IEEE/ACM** including *IEEE Distinguished Lecturer of IEEE Consumer Technology Society (CTSoc)*, *IEEE Distinguished Visitor of IEEE Computer Society (CS)* and *ACM India Eminent Speaker*.
2. He has been elected **Fellow of 3 professional engineering societies/institutes** (2 international societies and 1 from India) including *Fellow of IET (UK)*, *Fellow of British Computer Society (UK)* and *Fellow of IETE (India)*.
3. He has also been awarded **IEEE Computer Society Distinguished Visitor Medal/Coin (as Featured IEEE Distinguished Visitor)** - 2023.
4. Recipient of **8 Research/Technology Awards (nationally and internationally)** including *IEEE Chester Sall Memorial Consumer Electronics Award 2020 (Best Transactions paper of IEEE TCE)*, *IEEE CTSoc Best Research Award from IEEE CEM 2019*, *Best Paper Award in 37th IEEE ICCE 2019*, *IEEE Computer Society TC-VLSI Best Paper Award in IEEE iNIS 2017*, *Awardee of 'Springer Nature Best Research Award' - 9th IEEE iSES*, *Awardee of 'Best Research Paper Award' by IEEE Computer Society TC-VLSI - 8th IEEE iSES*, *Awardee of 'Best Research Award' by IIT Indore (2017)* and *Awardee of 'Best Technology Development Award' by IIT Indore (2023)*.
5. He held/holds **4 Chair positions in IEEE Societies/Technical Committees** including *Chair of IEEE Computer Society Distinguished Visitor Selection Committee*, *Former Chair and Executive Committee, IEEE Computer Society Technical Committee on VLSI (TC-VLSI)*, *Former Chair/Board Member, IEEE Consumer Technology Society Technical Committee on Security & Privacy (TC-SPC)* and *Founding Chair and Advisor, IEEE Consumer Technology Society Chapter- MP Section*.
6. He is the **Editor-in-Chief, IET Computers & Digital Techniques (CDT)** (from 2024 onward)
7. He is the former **Editor-in-Chief, IEEE VLSI Circuits & Systems Letter** (*IEEE Computer Society TCVLSI Newsletter*) (2018-2020)
8. He is the former **Deputy Editor-in-Chief, IET Computers & Digital Techniques** (from 2018 - 2024)
9. His authored books are **included and indexed in the libraries of some of the top universities of the world** such as: *Stanford University*, *University of Chicago*, *University of Colorado*, *University of Pittsburgh*, *University of Auckland, NZ*, *University of California, LA*, *National University of Australia*, *University of Wisconsin Madison*, *University of California, Riverside*, *University of Western Australia*, *New York University*, *Abu Dhabi*, *Keio University, Japan*, *Penn State University*.
10. Recipient of **2 Editor Awards from IEEE** including *Outstanding Editor Award from IEEE Consumer Technology Society - IEEE Consumer Electronics Magazine, Las Vegas (2019)* and *Associate Editor Award by IEEE Computer Society Technical Committee on VLSI (2018)*
11. He held/holds **22 Editorial positions in major IEEE/IET/Nature/Elsevier Journals** including *IEEE Transactions on Dependable and Secure Computing (TDSC)*, *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, *IEEE Transactions on Consumer Electronics*, *IEEE Transactions on Aerospace and Electronic Systems*, *IEEE Embedded Systems Letters (ESL)*, etc.
12. His **online course/technical materials and lectures (available in YouTube)** on various topics of "Computer Architecture" and "Hardware Security and IP Protection" have been cumulatively viewed over **17,000 times** and over **3,800 times** respectively so far, **leading to total views of over 21,000 times**
13. He is consistently ranked in **Stanford University's Top 2 % Scientists globally** (2021-2024).
14. His inventions and patents have over **18 industrial patent citations** from *IBM*, *Mathworks*, *Amazon*, *Siemens Corporation*, *Bigwood Technology*, *Extreme Scale Solutions*, *Atlantic Technical Organization*, *Calypto*, *Bluespec*, *BEECube*, *Huawei* etc.

15. He has developed over **8 security tools products in the domain of hardware security** which are open to scientific community (*available in IEEE CAD for Assurance*). **Accessed more than 8000 times**
https://www.anirban-sengupta.com/Hardware_Security_Tools.php
16. He has **supervised over 55 candidates** including 2 post-docs, 9 PhDs, 3 MS, > 30 B.Tech.
17. He has **6 sponsored projects and consultancy** from DST/SERB, CSIR, MEITY, UpGrad etc.

ADDITIONAL HIGHLIGHTS OF HIS ACHIEVEMENTS:

- ✓ **ACM India Eminent Speaker**, ACM India Council, 2025
- ✓ **Chester Sall Memorial Awardee**, IEEE Consumer Technology (Electronics) Society Best Transactions Paper, 2020 (**2nd Indian Scientist to receive this international prize**)
- ✓ **Chair**, IEEE Computer Society Distinguished Visitor Selection Committee (2025- Present) (**1st Indian Scientist to receive this honor**)
- ✓ **Editor-in-Chief**, IET Computers & Digital Techniques (CDT) (2024- Present) (**1st Indian Scientist to receive this honor**)
- ✓ **Editor Award by IEEE Consumer Technology Society** - IEEE Consumer Electronics Magazine, Las Vegas (2019) (**1st Indian Scientist to receive this honor**)
- ✓ **Associate Editor**, IEEE Transactions on Dependable and Secure Computing (TDSC) (**2nd Indian Scientist to receive this honor**)
- ✓ **Editorial Board Member**, Nature Scientific Reports
- ✓ **IEEE Computer Society Distinguished Visitor Committee**, IEEE Computer Society DVP
- ✓ **IEEE Computer Society Mission Enablement Committee (MEC)**, IEEE Computer Society
- ✓ **Former Chair and Executive Committee**, IEEE Computer Society Technical Committee on VLSI (TC-VLSI) (**1st Scientist from India to receive this honor**)
- ✓ **Former Chair/Board Member**, IEEE Consumer Technology Society Technical Committee on Security & Privacy (TC-SPC) (**1st Scientist from India to receive this honor**)
- ✓ **Visvesvaraya Fellow**, Ministry of Electronics & IT, Govt. of India
- ✓ Inventor of 16 Patents (USA, Canada and India)
- ✓ Author of 6 Books in the field of 'Hardware Security & Trust/IP Protection/CAD-VLSI'
- ✓ **Author of papers in top venues of IEEE, Nature, ACM and IET** - TVLSI, TCAD, TDSC, TCE, TAES, D&T, ESL, CASM, ITPro, CEM, Nature Scientific Reports, LOCS, Access, VCAL, Potentials, CDT, EL, ISQED, SOCC, AsianHOST, GLSVLSI, ISVLSI, ISCAS, DFTS, ASP-DAC, ICCE, VLSID, ICM, VLSI-DAT.
- ✓ **Senior Member of IEEE**
- ✓ Press/Media coverage of my research and invention - in Times of India, Economic Times, The Tribune, The Free Press Journal, THE WEEK, Devdiscourse. <https://www.anirban-sengupta.com/docs/News.pdf>
- ✓ **Former Guest Editor**, IEEE Transactions on CAD of Integrated Circuits and Systems(TCAD)
- ✓ **Selection Committee**, IEEE Consumer Technology Society Distinguished Lecturer Program (DLP)
- ✓ **Selection Committee**, Irish Research Council, Government of Ireland

Details can be found out in: <https://www.anirban-sengupta.com/>