



Technical Activity of IEEE Consumer Electronics Society Chapter - Bombay Section and IETE Sub-Center

Jan 30th, 2019 at Sapphire Hall, Hotel Sayaji, Indore

Hi-Tea/Snacks

Time: 9.30 am – 10.00 am

OPENING REMARKS

Time: 10.00 am – 10.05 am

Welcome remarks by Founder & Chair, IEEE CESoc chapter - Bombay Section

**Anirban Sengupta, Distinguished Speaker of IEEE CESoc
Assoc. Professor, Indian Institute of Technology (IIT) Indore**

Time: 10.05 am – 10.10 am

Remarks by Chapter Secretary, IEEE CESoc chapter - Bombay Section

Abhishek Srivastava, Assoc. Professor, Indian Institute of Technology (IIT) Indore

Time: 10.10 am – 10.15 am

Remarks by Chair, IETE Sub-Center and Treasurer, IEEE CESoc chapter - Bombay Section

Dhruva Ghai, Pro Vice-Chancellor, Oriental University

KEYNOTES AND TECHNICAL TALKS

Time: 10.15 am – 10.55 am

Keynote Talk 1: User Centered Design

**Ramesh Chandak,
Senior Technical Program Manager
Microsoft India, Hyderabad**

Time: 10.55 am – 11.35 am

Keynote Talk 2: Blockchain technology

**Pankaj Diwan
Director, BlockTitan Innovations Lab, Indore
Founder & CEO, IDEALABS FutureTech Ventures, Hyderabad**

Time: 11.35 am – 12.00 pm

Technical Talk 1: Functional Obfuscation using Robust Logic Locking and Encryption

**Anirban Sengupta
Assoc. Professor and IEEE Distinguished Speaker
Indian Institute of Technology (IIT) Indore**



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Time: 12.00 am – 12.20 pm

Technical Talk 2: Low-Overhead Highly Robust Embedded RTL Signature for DSP Core Protection: New Paradigm for Smart CE Design

Dipanjan Roy
Faculty, IIIT Lucknow

Time: 12.20 am – 12.40 pm

Technical Talk 3: Datapath Architecture Exploration in High Level Synthesis using GA and Firefly Algorithm

Pallabi Sarkar, Assistant Professor
VIT University Bhopal

CLOSING REMARK AND TOKEN OF APPRECIATION

Time: 12.40 pm – 12.50 pm

By Founder & Chair and Chapter Secretary of IEEE Consumer Electronics Society Chapter - Bombay Section

LUNCH

Time: 12.50 pm – 2 pm

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